



## THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Michael H. Perrott

Title:

METHOD AND APPARATUS FOR BIT ERROR RATE DETECTION

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Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, the undersigned brings the patents, publications, applications or other information identified in the attached:

Form(s) PTO-1449

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date of a national application of \$ 1.53(d) or within three month	tatement is filed within three months of the filing other than a continued prosecution application under hs of entry of the national stage as set forth in lication. Therefore, no fee is required.
before the mailing date of a fir date of a first Office action after under § 1.114. Therefore, no find the specified in § 1.97(b), the under § 1.97(b), the under § 1.97(b).	this Information Disclosure Statement is being filed at Office action on the merits or before the mailing er the filing of a request for continued examination fee is believed required.  Disclosure Statement is filed after the period ersigned hereby authorizes the Commissioner to 17(p) to Deposit Account No. 50-0631.
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<ul> <li>☑ deposited with the US Postal Service with sufficient postage as first class mail, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.</li> <li>☐ facsimile transmitted to the US Patent and Trademark Office.</li> </ul> Mull 7/2/03	Mark Zagorin, Reg. No. 36,067 Attorney for Applicant(s) (512) 347-9030 (512) 347-9031 (fax)

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